

Abstracts

A 40-Gbit/s optical repeater circuits using InAlAs/InGaAs HEMT digital IC chip set

M. Yoneyama, A. Sano, K. Hagimoto, T. Otsuji, K. Murata, Y. Imai, S. Yamaguchi, T. Enoki and E. Sano. "A 40-Gbit/s optical repeater circuits using InAlAs/InGaAs HEMT digital IC chip set." *1997 MTT-S International Microwave Symposium Digest 2. (1997 Vol. II [MWSYM])*: 461-464.

This paper describes an InAlAs/InGaAs HEMT digital IC chip set which includes a multiplexer, demultiplexer, decision circuit, and frequency divider. Electrically multiplexed and demultiplexed 40-Gbit/s transmission is successfully performed.

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